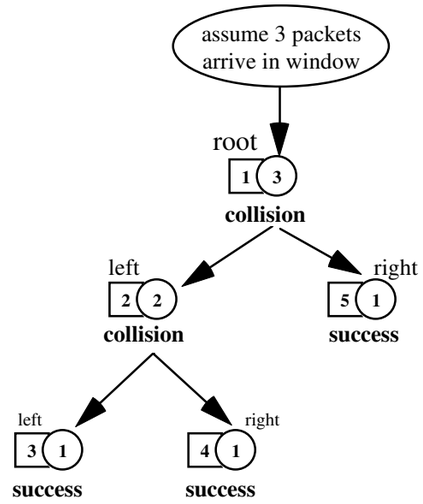
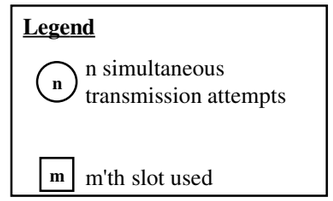
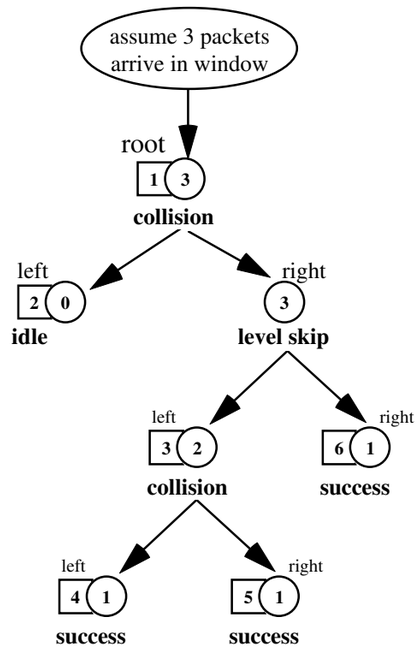
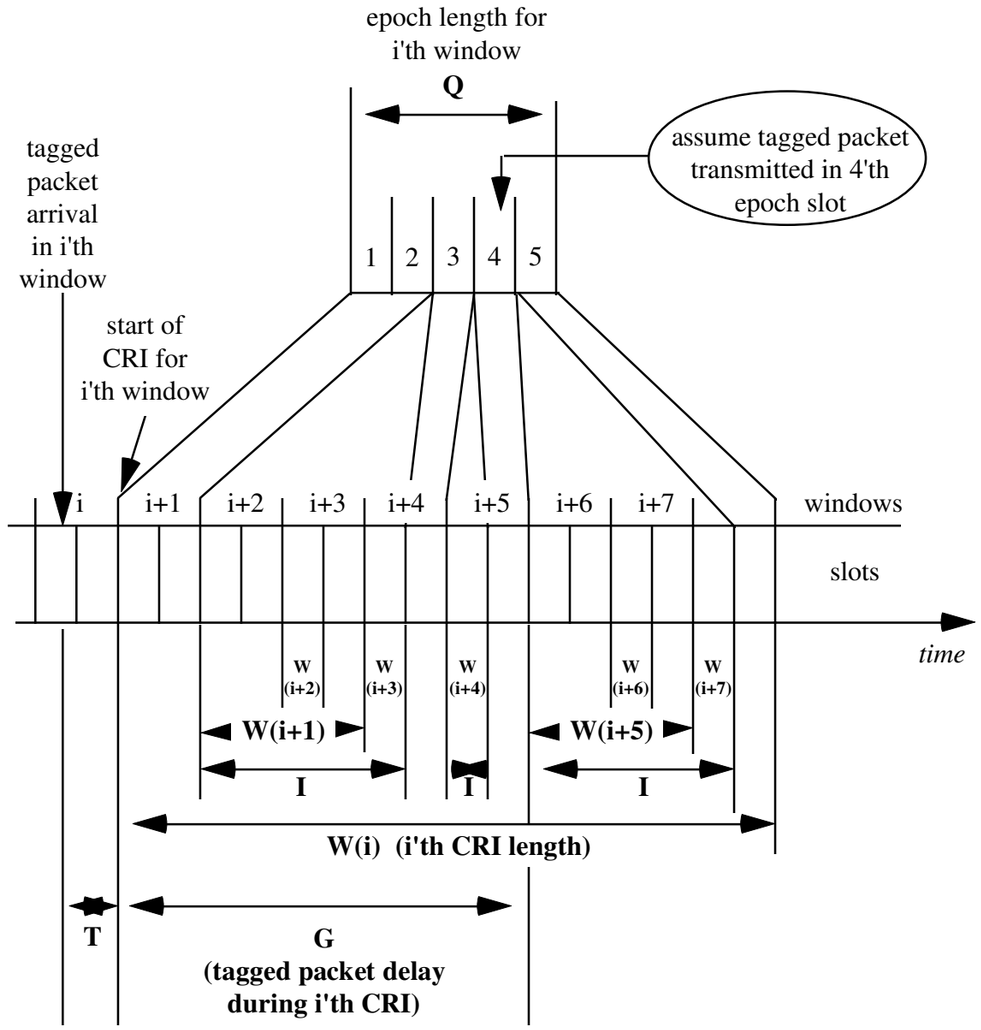
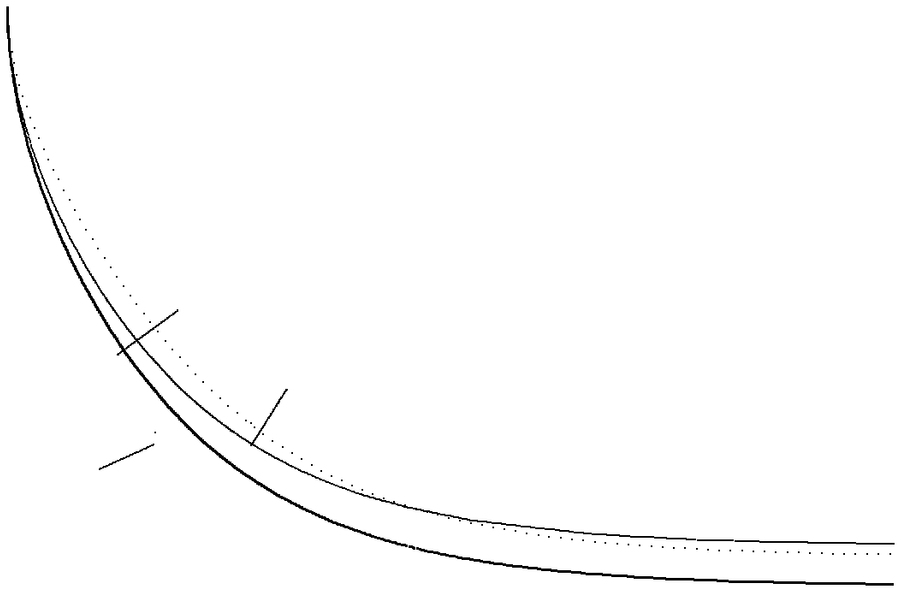
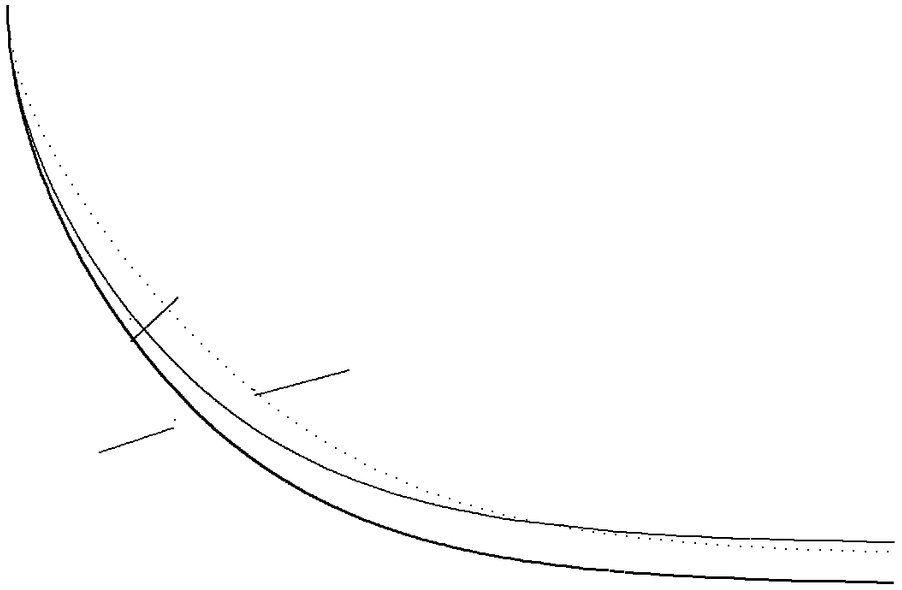
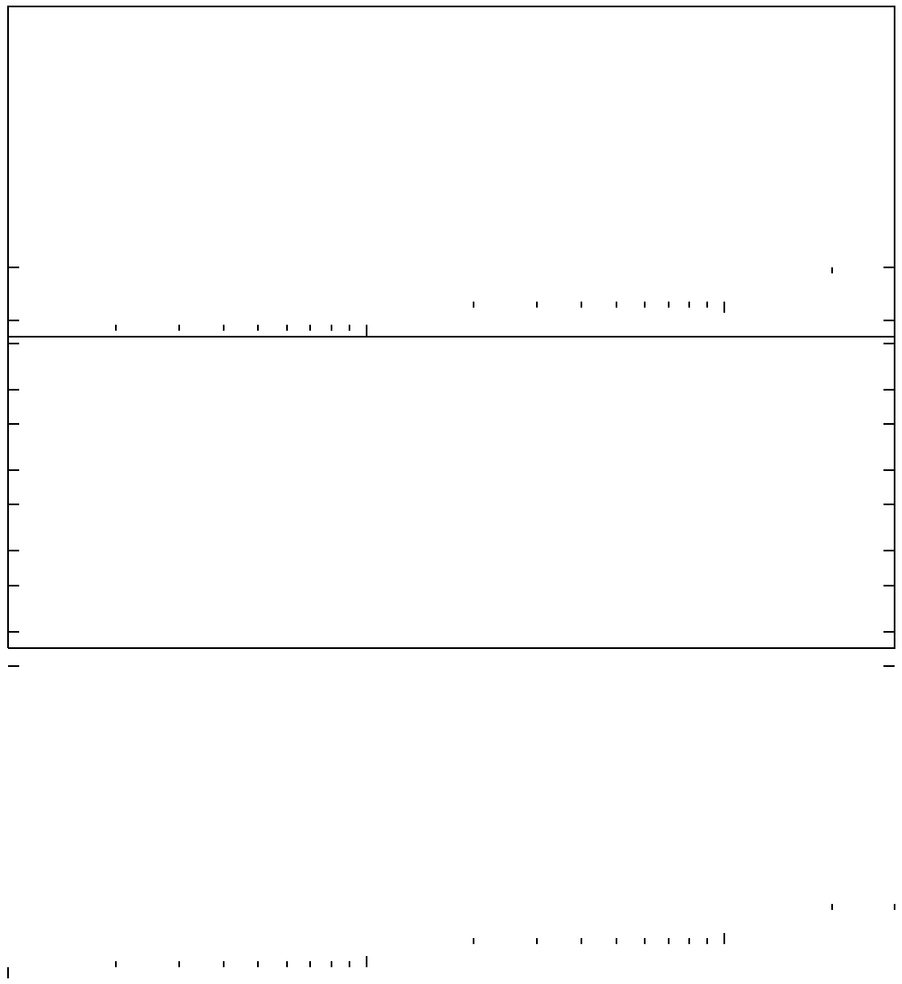
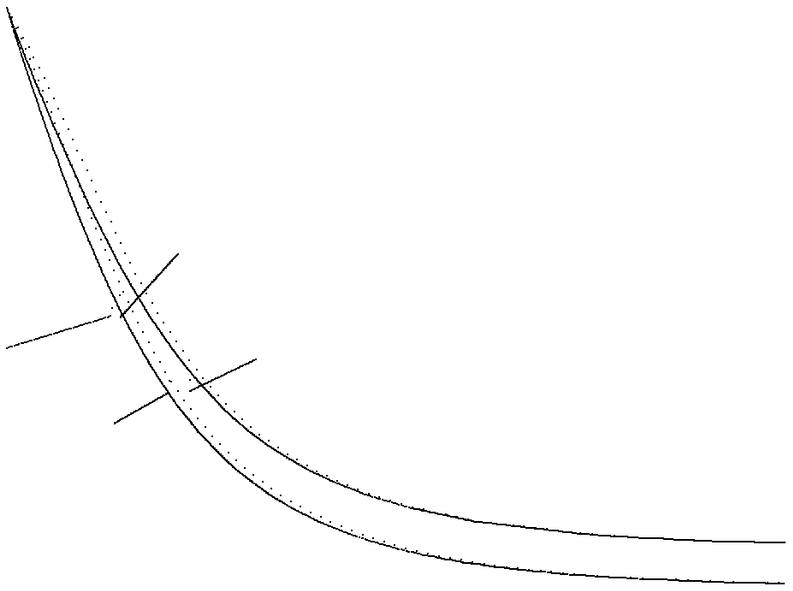
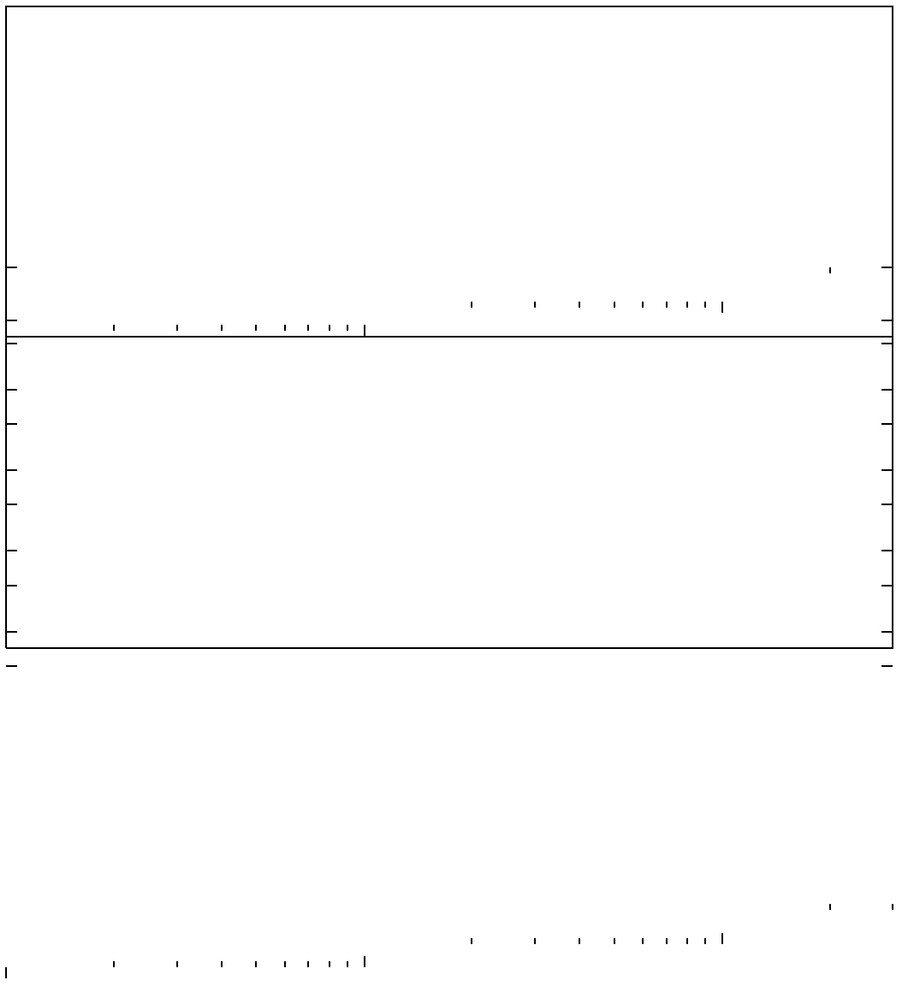
STA example: epoch length = 5





MTA example: epoch length = 6




epoch length for
i'th window
Q
assume tagged packet
transmitted in 4'th
epoch slot
tagged
packet
arrival
in i'th
window
1
2
3
4
5
start of
CRI for
i'th window
i
i+1
i+2
i+3
i+4
i+5
i+6
i+7
windows
slots
time
W
(i+2)
W
(i+3)
W
(i+4)
W
(i+6)
W
(i+7)
W(i+1)
W(i+5)
I
I
I
W(i) (i'th CRI length)
T
G
(tagged packet delay
during i'th CRI)